# **Exploiting Symmetry In Linear Time Temporal Logic Model Checking: One Step Beyond (Extended Abstract)**

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Abstract. Model checking is a useful technique to verify properties of dynamic systems but it has to cope with the state explosion problem. By simultaneous exploitation of symmetries of both the system and the property, the model checking can be performed on a reduced quotient structure [2,6,7]. In these techniques a property is specified within a temporal logic formula (CTL\*) and the symmetries of the formula are obtained by a syntactical checking. We show here that these approaches fail to capture symmetries in the LTL path subformulas. Thus we propose a more accurate method based on local symmetries of the associated Büchi automaton. We define an appropriate quotient structure for the synchronized product of the Büchi automaton and the global state transition graph. We prove that model checking can be performed over this quotient structure leading to efficient algorithms.

**Topic:** Formal Methods.

**Keywords:** Temporal Logic, LTL, Symmetries, Büchi automata, Model Checking, Verification.

#### 1. Introduction

Checking system correctness can be performed by the specification and the verification of temporal logic formulas over a state transition graph which models the system behavior. The well-known combinatorial explosion problem in space and time requires the development of some technique in order to reduce the size of the graph to be built, with respect to some desired properties.

One of the most promising technique has been initiated by Emerson & al [6,7]. It exploits the symmetries of both the system and formula. Such a technique builds a quotient graph in which each node represents an equivalent class of states. The relation is induced by a subgroup of permutations preserving the state graph and the formula. In practice, the permutations act on a set of system processes with identical behavior. Previous works have been already developed focusing on the safeness properties [1,14,17,19]. Other developments include model checking algorithms [2,13], model checking under fairness constraints [8] and application to system bisimulation [16].

Looking carefully at the technique described in [6,7], it appears that currently, the CTL\* model checking can make profit from the symmetries but in a restrictive way. Roughly speaking, two kinds of symmetries are detected: propositional symmetries (e.g. contain subformulas like  $f = V_{i \in I} f_i$  where  $f_i$  is a propositional formula involving the process i) and global symmetries with respect to a group of symmetries acting on the structure representing the formula (Büchi automata for linear temporal formulas like  $f = V_{i \in I} F f_i$ ). The aim of this paper is to generalize the previous methods showing how local symmetries can be exploited inside path subformulas of a CTL\* formula. In this work, we limit the presentation to the case of LTL formulas. The general framework for branching time model checking can be developed using the iterated method of [10]. Unlike the approach presented in [6,7], the considered Büchi automaton is not necessarily globally symmetric with respect to a predefined symmetry group. The starting point of our method is the analysis of any Büchi automaton associated with a LTL formula to be verified (see for instance [11]). Then we relate two states of the Büchi automaton if they represent the same current and future behavior up to a permutation of processes. Given a permutation, this state relation can be computed in polynomial time. Similarly, in the system model, two states are related by a permutation with respect to their current value of the system variables. By applying these relations on the synchronized product of the Büchi automaton and the global state transition graph, we define an appropriate quotient structure. Then we prove that model checking over this quotient is equivalent to model checking over the synchronized product. However, the general computation of permutations wipes out the benefit of having a quotient structure (computation with exponential complexity). Therefore, we propose an alternative approach which computes, in polynomial time, an intermediate size structure. Such a structure has the same equivalence property as the quotient one and leads, in practical cases, to significant savings of space (even exponential).

The next sections are organized as follows: part 2 presents the model of computation and briefly recalls the temporal logic used to specify properties, it also presents the representation of a linear temporal logic formula by means of a Büchi automaton; part 3 presents the definition of system symmetries while part 4 presents the symmetries reflected in a temporal logic formula; part 5 is the analysis of the model checking using symmetries and the proof of its validity; part 6 contains the operational model checking approach using symmetries; part 7 contains our conclusion and perspectives.

## 2. Model of Computation and Temporal Logic

We can apply our work on any system where symmetries are defined within a group of permutations. So, let us consider a simple model of system.

## 2.1. The Model

We deal with finite state concurrent systems composed of many processes. Processes are identified by indices. They may share global variables but differ from local ones. The structure of such a system is defined as follows:

#### **Definition 2.1.1: Finite State Concurrent System**

We present a finite state system using the temporal structure  $M=(S, \Delta, I, V, D, L, S_0)$  where:

- S is the finite set of the states;  $S_0 \subseteq S$  is the set of initial states;

- $\Delta \subseteq S \times S$  is the possible changes between states;
- *I* is the set of process indices;
- V is the set of system variables; It is composed of two distinct subsets,  $V_G$ , the set of global variables and  $V_L$  the set of local variables.
- D is the definition domain of variables.
- L is the state labeling function, L:  $S \times (V_G \cup V_L \times I) \to D$  such that:
  - (i)  $L(s, v_g)$  is the value of variable  $v_g$  of  $V_G$  in state s;
  - (ii)  $L(s, v_b i)$  is the value of variable  $v_l$  of  $V_L$  of process i in state s.

Atomic propositions are built from the association of a value to a variable.

#### **Definition 2.1.2: Global and Local Atomic propositions**

A global atomic proposition, is a pair  $(v_g, d) \in V_G \times D$  whereas a local atomic proposition is a triplet  $(v_I, i, d) \in V_I \times D$  that depends on a process i.

We define  $AP = \{p \mid p \in V_G \times D \cup V_L \times X \times D\}$  the set of atomic propositions built on the global and local variables.

We define prop:  $S \rightarrow 2^{AP}$  such that prop(s) is the set of propositions associated with s.

#### **Definition 2.1.3: Atomic propositions holding in a state**

Global (respectively local) atomic propositions hold at state s of S (noted  $\models$ ) as follows:  $s \models (v_g, d) \iff L(s, v_g) = d$ ; (respectively  $s \models (v_l, i, d) \iff L(s, v_l, i) = d$ ).

In the following we recall some notions of temporal logic used to specify system properties. The translation of linear temporal formulas to Büchi automata is also presented.

## 2.2. Temporal Logic

In a propositional Temporal Logic, the nontemporal portion of the logic is propositional logic. Thus formulas are build up from atomic propositions, which intuitively express, atomic facts about the underlying state of the concurrent system, truth-functional connectives and the temporal operators. Furthermore, when defining a system of temporal logic, two possible views of the system, can be considered, regarding the nature of time. One is that the course of time is linear: at each moment there is only one possible future moment. The other is that time has a branching tree-like nature: at each moment, time may split into alternate courses representing different possible futures. In linear time, one reasons about sets of infinite sequences, while in branching time, one reasons about the possible futures of the current state leading to branching tree like structure.

In our work we are mainly interested by the linear time temporal logic formulas. However, the notion of branching time temporal remains the general framework in which our model checking can be extended. We use here two kinds of operators, temporal operators presented later and path quantifiers using the two symbols, A, E, to indicate respectively all or some paths.

#### 2.2.1. Linear Temporal Logic (LTL)

A well-formed linear-time temporal logic, dealing with our system, is constructed from

the set of atomic propositions AP, the standard boolean operators V(Or),  $\neg(Not)$ , and the temporal operators X (neXttime) and U (strong Until). Precisely, formulas are defined inductively as follows:

- Every member of AP is a formula;
- if  $\phi$  and  $\psi$  are formulas then so are  $\neg \phi, \phi \lor \psi$ ,  $X\phi, \phi U \psi$ .

An interpretation for a linear-time temporal logic formula is an infinite word  $\xi = x_0 x_1$ ... over an alphabet  $2^{AP}$ . For more precision, the elements of  $2^{AP}$  are interpreted as assigning truth values to the elements of AP: elements in the set are assigned *true*, elements not in the set are assigned *false*. We note  $\xi_i$  the suffix of  $\xi$  starting at  $x_i$ . The semantics of LTL is defined in the following:

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-\xi \models \alpha \text{ iff } \boldsymbol{\alpha} \in x_0, \text{ for } \alpha \in AP.
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- $-\xi \models \neg \varphi \text{ iff } \neg (\xi \models \varphi).$
- $-\xi \models \phi \lor \psi \text{ iff } (\xi \models \phi \text{ or } \xi \models \psi).$
- $-\xi \models X\varphi \text{ iff } \xi_1 \models \varphi.$

As some abbreviations, one can introduce additional linear operators: the *eventuality* operator F where  $F\varphi = true\ U\ \varphi$ , the *always* operator G where  $G\varphi = \neg F \neg \varphi$ .

#### 2.2.2. From LTL to Büchi automata

A Büchi automaton is a finite automaton which accepts infinite sequences. A sequence is accepted if, and only if, it is recognized by the automaton and meets infinitely often one of the accepting states (called also designated states).

It has been shown that any LTL formula can be translated to a Büchi automaton in order to perform efficient model checking. Indeed, Büchi automata are strictly more expressive than LTL formulas and equivalent to linear-time Mu-calculus [5,18,21].

## Definition 2.2.3: Büchi automata

A Büchi automaton [6] is a tuple  $A=(AP, B, \rho, B_0, E, F)$  where:

- B is a set of states. Each state b of B is defined by the set  $Atom(b) \subseteq AP$ .
- $\rho$ :  $B \rightarrow 2^B$  is a nondetermistic transition function.
- $B_0 \subseteq B$  is a set of starting states.
- $E: B \rightarrow 2^{AP}$ .
- $F \subseteq B$  is a set of accepting states.

# 3. Symmetries on Models

Given a permutation  $\pi$ :  $I \rightarrow I$  on the set of process indices, we want to determine whether two states of the state transition graph are symmetric up to this permutation. Effectively, a permutation is said to be a symmetry if and only if it preserves the possible changes between states. We define the symmetries on the model represented by the structure  $M = (S, \Delta, I, V, D, L, S_0)$ .

## **Definition 3.1: Symmetry on a State Transition Graph**

(1) A permutation  $\pi$  on I, is a bijective mapping acting on S. For a given state  $s \in S$ ,  $\pi(s)$  is defined by:

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    (i)  ∀<sub>g</sub> ∈V<sub>G</sub>,L(π(s),ν<sub>g</sub>) = L(s,ν<sub>g</sub>);
    (ii)  ∀∈I, ∀<sub>l</sub> ∈V<sub>L</sub>,L(π(s),ν<sub>l</sub>,i) = L(s,ν<sub>l</sub>,π(i)).
    (2) Permutation π is a symmetry iff it satisfies the following condition:
    (∀<sub>l</sub> ∈S),(∀<sub>2</sub> ∈S),((s<sub>l</sub> →s<sub>2</sub>) ∈Δ ⇔(π(s<sub>l</sub>) →π(s<sub>2</sub>)) ∈Δ).
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The group of symmetries defined on M is called the automorphisms group of M and denoted Aut(M).

# 4. Symmetries on Formulas

In [7], the symmetries of a temporal logic formula to be verified are obtained by a syntactical checking while in [6], they result from the analysis of the corresponding Büchi automaton. By looking carefully at this method, it appears that symmetries of a CTL\* formula are obtained in a restrictive way. Roughly speaking, many techniques are proposed based on the detection of a group of symmetries:

- (1) State symmetries obtained from (sub)formulas like  $V_{i \in I}f_{i}$  where  $f_{i}$  is propositional involving process i. Effectively, the symmetries resulting from formulas like  $f = \mathrm{EF}(V_{i \in I}f_{i})$ ,  $f = \mathrm{EF}(\Lambda_{i \in I}f_{i})$  constitute the group Sym(I), the group of all the permutations between the elements of I. Those computed for formula like  $f = \mathrm{EF}f_{i}$  constitute the group Stab(i) (the group of all the permutation between the elements of  $I \setminus \{i\}$ ).
- (2) The former approach fails to capture *Path symmetries* in LTL subformulas like f= V<sub>i</sub> ε<sub>I</sub>Ff<sub>i</sub>, f= Λ<sub>i</sub> ε<sub>I</sub>Ff<sub>i</sub>. This case is presented in [13]. Moreover, the method of [6] introduce a complementary framework by detecting a group of symmetries acting on the state of Büchi automaton.

All these approaches are inefficient for formulas like  $f = A_{i,j \in I}^{i < j}(f_i U f_j)$  because the group of symmetries is reduced to the identity. However, the former formula contains local symmetries that can be reflected in some states of its büchi automaton.

In this section, we propose a more accurate method based on the exploitation of local symmetries computed for some states of the automaton. Hence, we show that the computation of a group is not required.

We compute, the symmetries on a Büchi automaton,  $A=(AP, B, \rho, B_0, E, F)$ . The states equivalence can be detected using the relation defined as follows:

## Definition 4.1: Equivalence of two states of a Büchi automaton

A relation  $\mathcal{R}_{\pi}$  is the coarsest relation that defines the equivalence of two states of a Büchi automaton. It fulfills the following two requirements:  $bb,b' \in V,b\mathcal{R},b'$  iff (1) There is a permutation  $\pi$  between b,b' that satisfy the two conditions:

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- b \in \mathcal{F} \iff ' \in \mathcal{F};

- Atom(b) = Atom(\pi(b)) =

\{(v_g, d) \mid \forall (v_g, d) \in Atom(b), \exists (v_g, d) \in Atom(b) \}  \forall (v_l, j, d) \mid \forall (v_l, i, d) \in Atom(b), \exists (v_l, j, d) \notin Atom(b) \}  \forall (v_l, i, d) \in Atom(b), \exists (v_l, i, d) \notin Atom(b) \}  \forall (v_l, i, d) \in Atom(b), \exists (v_l, i, d) \notin Atom(b) \}  (2) \forall (v_l, i, d) \in Atom(b), \exists (v_l, i, d) \notin Atom(b) \}  \forall (v_l, i, d) \in Atom(b), \exists (v_l, i, d) \notin Atom(b) \}  \forall (v_l, i, d) \in Atom(b), \exists (v_l, i, d) \notin Atom(b) \}  (3)
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Generally,  $\mathcal{R}_{\pi}$  is not an equivalence relation. It can be computed in a polynomial time using a fixpoint computation starting with condition (1) and by applying condition (2).

**Example 1:** Let us consider the following Büchi automaton representing the formula  $f = [(p_1 U p_3) \lor (p_2 U p_3)] \land (p_1 U p_2)$  for a system of three processes  $P_1$ ,  $P_2$ ,  $P_3$ , where  $p_1$ ,  $p_2$ ,  $p_3$  are three atomic propositions.

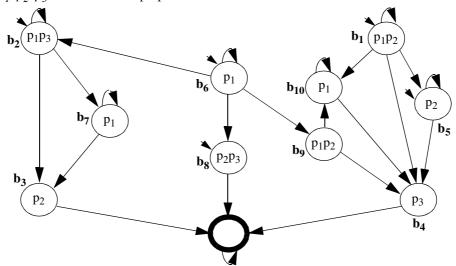


Figure 1: Büchi automaton of the formula f

In this automaton, the only global symmetry group acting on the states is the identity. However, one can observe that states  $b_7$  and  $b_{10}$  are symmetrical with respect to definition 4.1 (permutation  $\pi$  such that  $\pi(1)=1$ ,  $\pi(2)=3$ ,  $\pi(3)=2$  is used). Similarly, other symmetries can be detected between  $b_3$  and  $b_4$ ,  $b_5$  and  $b_{10}$ ,  $b_1$  and  $b_9$  etc. Conversely,  $b_7$  and  $b_6$  are identically labelled but not symmetrical.

In the next section, we show how to perform an efficient model checking using the Büchi automaton representation and the proposed symmetries.

# 5. Analysis of Model Checking using symmetries

Classically, model checking is realized by (1) considering the Büchi automaton,  $A_{\neg f}$  of the negation of a formula f; (2) building the synchronized product of this automaton and the one which models the behavior of the system; (3) searching in the synchronized product a sequence which has an accepting state repeated infinitely often in order to prove that the negation of the formula holds. The meaning of such algorithm is that one must verify that any behavior of the system validates the formula. This algorithm can work in an "on-the-fly" fashion [11] so as to avoid the construction of the whole graph of the strongly connected components.

#### 5.1. Synchronized Product

The synchronized product of M and  $A_{\neg f}$  is noted  $M \times A_{\neg f}$  and is defined as follows:

#### **Definition 5.1.1: Synchronized Product**

The synchronized product of  $M=(S, \Delta, I, V, D, L, S_0)$  and  $A_{\neg f}=(AP, B, \rho, B_0, E, F)$  is the automaton  $M\times A_{\neg f}=(AP, \Theta, \Gamma, \Theta_0, \Phi)$  defined in the following:

```
\begin{array}{ll} -\Theta = \; \{(s,b) \big| s \in S \; \land b \in B \; \land (E(b) \subseteq prop(s))\} \; ; \\ -\Theta_0 = \; \{(s,b) \big| s \in S_0 \; \land b \in B_0 \; \land (E(b_0) \subseteq prop(s_0))\} \; ; \\ -\; ((s_i,b_i) \rightarrow (s_j,b_j)) \in \Gamma \; \text{ iff } \; (s_i \rightarrow s_j) \in \Delta \; \land (b_i \rightarrow b_j) \in \rho \; . \\ -\Phi = \; \{(s,b) \big| s \in S \; \land b \in F \; \land (E(b) \subseteq prop(s))\} \; . \end{array}
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By means of such a product, a formula f holds through M, if and only if there is no path, in  $M \times A_{\neg f}$ , in which an accepting state is repeated infinitely often. Classically, the satisfaction of a formula is expressed as follows:

$$\mathbf{M} \models \neg f \iff \mathbf{p} = (s_0, b_0) \dots (s_l, b_l) \dots (s_m, b_m) \dots (s_n, b_n) \text{ in } M \times A_{\neg f} \text{ where } l \leq n < n$$
such that: (1)  $b_m \in F$ ; (2)  $(\forall i, j \in I, i \not= j, ((s_i, b_i) = (s_i, b_i) \iff (i, j) = \{l, n\})$ )

## 5.2. Quotient Structure

In order to reduce the size of the synchronized product structure, we only consider canonical representatives of the symmetrical states instead of all the states. Consequently, we build a graph of representatives with respect to a symmetry relation,  $\mathcal{R}$ , defined on  $M \times A_{\neg f}$  as follows:

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Definition 5.2.1: Symmetry Relation, \mathcal{R}, Defined on M \times A_{-f} \not \boxtimes_s ' \in S, \not \boxtimes_b b' \in V such that s \models Atom(b) and s \models Atom(b), (s,b)\mathcal{R}(s',b)' iff \exists \pi \in Aut(M) such that b\mathcal{R}_s b' and \pi(s) = s.
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Observe that  $\mathcal{R}$  is an equivalence relation since it is defined on the group Aut(M). Therefore, we can define the quotient structure of the synchronized product  $M \times A_{\neg f}$  denoted  $\overline{M \times A_{\neg f}} = (M \times A_{\neg f})/\mathcal{R}$  as follows:

# Definition 5.2.2: The Quotient Structure $\overline{M \times A_{-f}}$

The quotient structure  $\overline{M \times A_{-f}}$  is defined by means of the representatives of the state orbits of  $M \times A_{-f}$ . The orbit of  $(s,b) \in \Theta$  is defined by the set:

```
\mathcal{A}(s,b) = \{(s,b) \mid \exists \pi \in Aut(M), (\pi(s) = s) \land (b\mathcal{R}_{\mathcal{A}}b') \text{ where, } s' \models Atom(b) \}. From each orbit \theta(s,b), we pick an arbitrary representative denoted (s,b).
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The representative can be efficiently implemented by defining a canonical representation based on a lexicographical order [1].

# 5.3. Model Checking Correctness

In this section we validate our approach by showing that the model checking based on the proposed quotient synchronized product is equivalent to the one performed by means of the ordinary structure. Intuitively, we prove that the existence of an accepting state repeated infinitely often in the quotient structure is equivalent to the existence of an accepting state repeated infinitely often in the ordinary synchronized product. Hence, we can prove the satisfaction of temporal logic formulas by using our approach of symmetry.

We start our proof by the correspondence between both the quotient and the ordinary structures of the synchronized product.

Let  $M \times A_{\neg f}$  represents the structure resulting from the synchronized product of the state transition graph and the automaton and let  $\overline{M \times A_{\neg f}} = (M \times A_{\neg f})/\mathcal{R}$  be its quotient structure with respect to the relation  $\mathcal{R}$  introduced in definition 5.2.1:

#### Lemma: Correspondence Lemma

$$\exists \overline{(s_0,b_0)},...,\overline{(s_n,b_n)} \in \overline{M \times A_{\neg f}} \Leftrightarrow \exists (s'_0,b'_0),...,(s'_n,b'_n) \in M \times A_{\neg f} \text{ such that}$$

$$0 \leq \forall i \leq n, (s'_i,b'_i) \Re(s_i,b_i)$$

#### **Proof:**

the \( \)direction is immediate from the definition of quotient structure.

For the  $\Rightarrow$  direction, we proceed by induction on, n, the length of the path:

- (i) n=0, This case is very simple since for any  $(s'_0,b'_0)$  such that  $(s'_0,b'_0)\mathcal{R}(s_0,b_0)$ , the lemma is proved.
- (ii) We assume that the lemma is proved for a given length equal to n and we verify that it is proved for a length equal to n+1. Let us consider  $(s_0,b_0)$ , ...,  $(s_n,b_n)$  in  $\overline{M \times A_{\neg f}}$ , and let us recall that by assumption, we have  $(s'_0,b'_0)$ , ...,  $(s'_n,b'_n)$  in  $M \times A_{\neg f}$  such that  $0 \le M \le n$ ,  $(s'_i,b'_i) \mathcal{R}(s_i,b_i)$ . Let us consider an edge  $(s_n,b_n) \to (s_{n+1},b_{n+1})$  of  $\overline{M \times A_{\neg f}}$ , thus,  $\exists (s''_n,b''_n) \to (s''_{n+1},b''_{n+1})$  in  $M \times A_{\neg f}$  such that  $(s''_n,b''_n)\mathcal{R}(s_n,b_n)$  and  $(s''_{n+1},b''_{n+1})\mathcal{R}(s_{n+1},b_{n+1})$ . We have already  $(s'_n,b'_n)\mathcal{R}(s_n,b_n)$ . So,  $(s'_n,b'_n)\mathcal{R}(s''_n,b''_n)$  and from the definition of the relation  $\mathcal{R}$ :  $\exists n,s'_n=n,s''_n$  and  $b'_n\mathcal{R}_n \mathcal{P}''_n$ . Hence, there is a state  $s'_{n+1}=n,s''_{n+1}$  and an automaton state  $b'_{n+1}$  such that  $b'_{n+1}\mathcal{R}_n \mathcal{P}''_{n+1}$  where  $s'_{n+1}=n,s''_{n+1}$  and since, there is an arc  $(s''_n,b''_n) \to (s''_{n+1},b''_{n+1})$ , the arc  $(s''_n,b''_n) \to (s''_{n+1},b_{n+1})$  is an arc of the same structure also.

From this lemma, we now prove the model checking equivalence:

Theorem: The two following statements are equivalent:

(i) There is a path 
$$(\overline{s_0,b_0}) ... (\overline{s_l,b_l}) ... (\overline{s_m,b_m}) ... (\overline{s_n,b_n})$$
 in  $\overline{M} \times A_{-f}$  where  $l \le m < n$  such that: (1)  $\overline{b_m} \in F$ ; (2)  $\forall i \ne j, ((\overline{s_i,b_i}) = (\overline{s_i,b_i})) \rightleftharpoons (\{i,j\} = \{l,n\})$ .

- (ii) There is a path  $(s'_{\theta},b'_{\theta})\dots(s'_{l},b'_{l})\dots(s'_{m},b'_{m})\dots(s'_{n},b'_{n})$  in  $M\times A_{\neg f}$  where  $l \leq m < n$  such that: (1)  $b_{m} \in F$ ; (2)  $\forall i \not = f((s'_{i},b'_{i}) = (s'_{j},b'_{j})) \notin (\{i,j\} = \{l,n\})$ . Proof:
- (1) (i)  $\Rightarrow$  (ii): Let us consider  $(\overline{s_0,b_0}) \dots (\overline{s_l,b_l}) \dots (\overline{s_m,b_m}) \dots (\overline{s_n,b_n})$  the shortest path that verifies (i1) in  $\overline{M} \times A_{-f}$ . From the correspondence lemma there is a corresponding path  $(s'_0,b'_0),\dots,(s'_l,b'_l),\dots,(s'_m,b'_m),\dots,(s'_n,b'_n)$  in  $M \times A_{-f}$ . We prove that it verifies the following two statements:
- (ii1)  $b'_m \in F$ , effectively, symmetries built on the automaton preserve accepting states;
- (ii2) Two directions have to be proved. The  $\Rightarrow$  direction is straightforward since  $(s'_i,b'_i)=(s'_j,b'_j)$   $\Rightarrow$   $((\overline{s_i,b_i})=(\overline{s_j,b_j}))$ , consequently we deduce from (i2 that  $\{i,j\}=\{l,n\}$ . For the  $\Leftarrow$  direction, we have  $\{i,j\}=\{l,n\}$ , so from (i2),  $(\overline{s_i,b_i})=(\overline{s_j,b_j})$ . By assumption,  $\{i,j\}=\{l,n\}$ , so,  $(\overline{s_l,b_l})=(\overline{s_n,b_n})$  from (i2). The proof is now made by contradiction assuming that  $(s'_l,b'_l)\neq (s'_m,b'_m)$ . In this case,  $(s'_l,b'_l)\Re(s'_m,b'_m)$  since their representatives are equals. Consequently, from the correspondence lemma, we have an infinite path in which there is an infinite number of subpaths of the form

 $(s_{n+k(n-l)}, b_{n+k(n-l)}) \dots (s_{n+(k+l)(n-l)}, b_{n+(k+l)(n-l)})$  where k=0,1,... and for  $l \le h \le n$ ,  $(s_{i+k(n-l)}, b_{i+k(n-l)}) \mathcal{R}(s_i, b_i)$ . Because we deal with finite state transition graph, this infinite path must contain a circuit. Let us consider  $(s_0,b_0),...,(s_x,b_x),...,(s_y,b_y)$  the shortest path that meets twice the same state where:  $(s_x, b_x) = (s_y, b_y)$  and  $x \ge y$ . To simplify the proof we consider x < y and we denote f(x) = x - k(n-l), f(y) = y - k(n-l) such that  $f(x) f(y) \in [l, n]$ . Since  $((s_{x},b_{x})=(s_{y},b_{y})) \Rightarrow ((\overline{s_{x},b_{x}})=(\overline{s_{y},b_{y}})) \Rightarrow (\overline{s_{f(x)},b_{f(x)}})=(\overline{s_{f(y)},b_{f(y)}})$ have two cases. In the first one: f(x), f(y) are from [l,m[or]m,n]. By considering  $(s_0, b_0), ..., (s_{l}, b_{l}), ..., (s_{f(x)}, b_{f(x)}), (s_{f(y)+1}, b_{f(y)+1}), ..., (s_{m}, b_{m}), (s_{m}, b_{m})$  $(s_0,b_0),...,(s_l,b_l),...,(s_m,b_m),(s_{f(x)},b_{f(x)}),(s_{f(y)+1},b_{f(y)+1}),...,(s_n,b_n)$ with respect to the position of f(x), f(y) in the domain, we have a shortest path than  $(s'_0,b'_0),...,(s'_l,b'_l),...,(s'_m,b'_m),(s'_n,b'_n)$  that verifies the two conditions (ii1) and (ii2) which is opposite to the initial assumption. In the second case: f(x)is from [l,m[ and f(y) from [m,n]. By considering the following path that verify  $(s_0, b_0), \dots, (s_{f(x)}, b_{f(x)}), \dots, (s_m, b_m), \dots, (s_{f(y)}, b_{f(y)}), \text{ we}$ (ii1) and (ii2), have a shortest path than  $(s'_0,b'_0),...,(s'_l,b'_l),...,(s'_m,b'_m),(s'_n,b'_n)$  which is contradictory with the initial assumption. Consequently  $(s'_l,b'_l)=(s'_m,b'_m)$ .

(2) (ii)  $\Rightarrow$  (i): Let us consider the set of paths:  $\Pi = \{(s_0, b_0)...(s_b, b_l)...(s_m, b_m)...(s_m, b_n) \text{ such that } (s_b, b_l) \Re(s_m, b_m) \text{ and } b_m \in F \}.$ 

 $\Pi \neq \mathbb{Z}$  from the assumption (i1). Let us consider  $\pi = (s_0, b_0) ... (s_n, b_n)$  one of the shortest path of  $\Pi$ . From the correspondence lemma,  $\pi = (\overline{s_0, b_0}) \dots (\overline{s_n, b_n})$  a representative path where  $(s_i,b_i)R(s_i,b_i)$ . Consequently,  $\overline{\pi}$  verifies (i1)  $(\overline{s_{l}}, \overline{b_{l}}) = (\overline{s_{n}}, \overline{b_{n}})$  and (i2)  $b'_{m} \in F$ . We have to prove  $\forall \{i,j\} \neq \{l,n\} \Rightarrow (\overline{s'_i,b'_i}) \neq (\overline{s'_i,b'_i}).$ suppose  $\exists \{i,j\} \neq \{l,n\}, (\overline{s'_i,b'_i}) = (\overline{s'_i,b'_i})$ , hence, either,  $(s'_i,b'_i) = (s'_i,b'_i)$  which is impossible from (ii2), or,  $(s'_i,b'_i)\mathcal{R}(s'_i,b'_i)$ . Hence, three cases can appear. in the first one we have  $(i \not \triangleleft \not \supseteq) \lor (l \not \supseteq \not \triangleleft \not \lhd m) \lor (m \not \triangleleft \not \triangleleft \not \lhd n)$ : by considering the path  $(s_0,b_0)...(s_i,b_i)(s_{i+1},b_{i+1})...(s_n,b_n)$  we have a shortest path than  $\pi$  belonging to  $\Pi$ which is opposite to the initial assumption. In the second case we have i < l < j < m < n: by considering  $(s_0, b_0) ... (s_i, b_i) (s_{j+1}, b_{j+1}) ... (s_n, b_n) (s_{l+1}, b_{l+1}) ... (s_j, b_j)$ we have a shortest path than  $\pi$  belonging to  $\Pi$  which is opposite to the initial assumption. In the last case we have  $l \le j \le m \le j \le n$ : by considering  $(s_0,b_0)...(s_i,b_i)...(s_m,b_m)...(s_i,b_i)...(s_n,b_n)$  we have also a shortest path than  $\pi$  belonging to  $\Pi$  which is contradictory for the initial assumption. Consequently,  $\forall \{i,j\} \neq \{l,n\} \Rightarrow (\overline{s'_i,b'_i}) \neq (\overline{s'_i,b'_i})$ .

## 5.4. Consistent Graph

The quotient structure is the smallest structure that can be built to perform model checking using symmetries. In the worst case, it requires an exponential time construction therefore, we propose a new approach based on the construction of an intermediate structure, called *consistent graph* which does not require the computation of all the symmetries.

In such a graph, reachability is preserved with respect to the ordinary synchronized product. Moreover, the transition relation of the ordinary synchronized product is preserved accordingly to the symmetry relation  $\mathcal{R}$  in the consistent graph and vice versa. Hence, All paths are preserved with respect to  $\mathcal{R}$ .

Such a consistent graph will be used in section to propose an efficient model checking in polynomial time.

## **Definition 5.4.1: Consistent Graph**

- (1)  $\[ \mathcal{K}(s,b) \in \Theta \]$  such that (s,b) is reachable from  $(s_0,b_0) \in \Theta \]$ ,  $\[ \mathcal{J}(s,b) \in \Theta' \]$  reachable from  $(s'_0,b'_0) \in \Theta \]$  such that  $(s,b) \in \Theta \]$  such that  $(s,b) \in \Theta \]$ .
- (2)  $\forall (s,b) \in \Theta$  such that (s,b) is reachable from  $(s_0,b_0) \in \Theta_0$ ,  $\forall (s,b) \in \Theta'$  reachable from  $(s'_0,b'_0) \in \Theta_0$  such that  $(s,b) \notin \Re(s,b)$ :

if 
$$(s,b) \rightarrow (s_1,b_1) \in \tau$$
 then  $\mathcal{A}(s_1',b_1') \in \Theta'$  where  $(s_1',b_1')\mathcal{R}(s_1,b_1)$  such that  $(s',b') \rightarrow (s'_1,b'_1) \in \tau'$ .

(3)  $\forall (s,b) \in \Theta$  such that (s,b) is reachable from  $(s_0,b_0) \in \Theta_0$ ,  $\forall (s,b) \in \Theta'$  reachable from  $(s'_0,b'_0) \in \Theta_0$  such that  $(s,b) \notin \Re(s,b)$ :

if 
$$(s',b') \rightarrow (s'_1,b'_1) \in \tau'$$
 then  $\mathcal{I}(s_1,b_1) \in \Theta$  where  $(s_1,b_1)\mathcal{R}(s'_1,b'_1)$  such that  $(s,b) \rightarrow (s_1,b_1) \in \tau$ .

The following lemma highlights the correspondence between the quotient structure  $\overline{M \times A_{\neg f}}$  and the graph consistent with  $M \times A_{\neg f}$ .

## **Lemma: Consistent Graph Correspondence**

$$\exists (s_0, b_0), ..., (s_n, b_n) \in \overline{M \times A_{\neg f}} \Leftrightarrow \exists (s'_0, b'_0), ..., (s'_n, b'_n) \in G'$$
 consistent with  $G = M \times A_{\neg f}$  such that  $0 \leq M \leq n, (s'_i, b'_i) \Re(s_i, b_i)$ .

**Proof:** It is similar to the one used for the correspondence lemma (section 5.3).

Hence, we can prove the model checking equivalence between the quotient structure and the graph consistent with the ordinary structure using correspondence lemma of the consistent graph.

#### Theorem: The two following statements are equivalent:

(i) There is a path  $(\overline{s_0 b_0}) ... (\overline{s_l b_l}) ... (\overline{s_m b_m}) ... (\overline{s_n b_n})$  in  $G = \overline{M} \times A_{\neg f}$  where  $l \le n < n$  such that:

$$(1) \ \overline{b_m} \in F; \qquad (2) \quad \forall i \not = ((\overline{s_i,b_i}) = (\overline{s_i,b_i})) \in (\{i,j\} = \{l,n\}) \ .$$

(ii) There is a path  $(s'_{\theta},b'_{\theta})\dots(s'_{l},b'_{l})\dots(s'_{m},b'_{m})\dots(s'_{n},b'_{n})$  in G consistent with  $G=M\times A_{-f}$  where  $l\leq m < n$  such that:

$$(1) \ b_m \in F; \qquad (2) \quad \forall i \not\preceq j, ((s'_i,b'_i)=(s'_j,b'_j)) \ \Longleftrightarrow (\{i,j\}=\{l,n\}) \ .$$

**Proof:** This proof is similar to the one presented in the theorem of model checking equivalence (section 5.3) using the consistent graph correspondence lemma.

## 6. Operational Approach

The construction of a quotient structure is performed by checking, for each node built during the synchronized product, whether it is symmetrical with an already computed one. For this, an equivalence test has to be performed in an exponential time O(n!) (in the worst case), where n is the number of processes. Clearly, this would damage the benefit to have a condensed structure.

The following section introduces an operational approach in order to reduce the complexity of the construction algorithms. Nevertheless, the resulting state transition graph may have a larger size than the quotient structure because we compute only a reduced subset of symmetries. However, it is a consistent structure with both the ordinary and the former quotient structure, thus the model checking can be performed equivalently. This section aims at presenting efficient algorithms to compute symmetries and to construct the consistent graph.

# 6.1. ij-Symmetry on Büchi automata

We now define a set of symmetries called the *ij-symmetries* in such a way that it represents a subset of  $\mathcal{R}$ ,

#### 6.1.1. ij-Symmetry Definition

Let  $A = (AP, B, \rho, B_0, E, F)$ . A relation  $\mathcal{R}_{i,j}$  is a coarse relation that defines the equivalence of two states of a Büchi automaton with respect to two given processes. It fulfills the following definition:

 $bb, b' \in B, bR_{i,i}b$  iff the following two conditions hold:

```
(1) \begin{subarray}{ll} \be
```

From this definition we define an inner symmetry,  $\mathcal{R}_{in}$ , with respect to I as follows:  $\forall i,j \in I, (b,i) \mathcal{R}_{in}(b',j) \iff \mathcal{R}_{i,j}b'$  where  $\mathcal{R}_{in} = \left(\bigcup_{i=1}^n \mathcal{R}_{i,j}\right)^*$ . Hence, the inner symmetries presented by the relation  $\mathcal{R}_{in}$  constitutes a subset of the set of symmetries presented by  $\mathcal{R}$  such that:  $b\mathcal{R}_{in}b' \implies b\mathcal{R}b'$ . Based on  $\mathcal{R}_{in}$ , the next section proposes an efficient algorithm for the determination of the symmetries which are reflected in a Büchi automaton.

## 6.1.2. ij-Symmetry Computation Algorithms

The computation of the inner symmetry starts from the computation of the *ij-symmetry* presented in the definition .

Let f be a temporal specification formula and let  $A_{\neg f}$  be the representation of its negation in terms of Büchi automaton.

Firstly, we calculate, for a pair (i,j) of process indices, an initial partition of the states of  $A_{\neg f}$  using the definition of *ij-permutation*. This results in a set of state pairs that verify the *ij-permutation*.

Secondly, we restrain the computed *ij-permutations* to  $\mathcal{R}_{i,j}$  symmetry by checking the preservation of the transition relation (using the definition). For each state, we save the set of  $\mathcal{R}_{i,j}$  symmetries.

This algorithm is repeated for each pair *i,j* of process indices.

Let  $B = \{b_1 \dots b_m\}$  be the states of  $A_{\neg f}$  where |B| = m is the number of states. Let I be the set of process indices such that |I| = n. We construct a set of boolean matrices that represent the symmetry relation between the states of B. We note  $Mat_{ij}$  the matrix representing the symmetry  $\mathcal{R}_{i,j}$  where  $Mat_{ij}[b_d, b_k]$  is true if  $b_d$ ,  $b_k$  are symmetric. We note Mat the set of all the matrices  $Mat_{ij}$ .

In the following, we present the function  $R_{ij}$  which compute the ij-symmetry in the automaton. This function calls two functions: the first one ij-permut $(b_1,b_2)$  which checks the ij-permutation for two given states  $b_1$  and  $b_2$ . The second one  $R_{ij}(b_1,b_2)$  which checks if the two states,  $b_1$ ,  $b_2$ , are ij-symmetric. Note that The function Succ(b) computes all the successors of a given state b.

It must be noted that the complexity of the following algorithm is  $O(m^5)$  for a given i,j. Hence, The determination of all the ij-symmetry have a complexity of  $O(n^2 \times m^5)$  which means a polynomial complexity. Furthermore, the computation of  $\mathcal{R}_{in} = \left[ \bigcup_{i=1}^{n} \mathcal{R}_{i,j} \right]^*$  can be restricted to have, also, a polynomial complexity.

```
Algorithm 1: R_{ij}()
Output : A boolean value indicates if the two states are ij -symmetric.

BEGIN

FOR each b_d from V DO

FOR each b_k from V DO

Mat<sub>ij</sub>[b_d, b_k] = ij-permut(b_d, b_k);

FOR each b_d from V DO

FOR each b_k from V DO

Mat<sub>ij</sub>[b_d, b_k] = R_{ij}(b_d, b_k);

END;
```

```
Algorithm 2: R_{ij}(b_d, b_k)
Output : A boolean value indicates if the two states are ij -symmetric.

BEGIN

Loop = TRUE;
WHILE Loop DO

BEGIN

IF Mat_{ij}[b_d, b_k] THEN

FOR [each\ b_d\ in\ Succ(b_d)] AND [each\ b_k\ in\ Succ(b_k)] DO

R_{ij}(b_d\ ,b_k\ );
ELSE

Loop = FALSE;
END;
END. /* Algorithm */
```

## 6.2. Construction of the Consistent Graph

We compute the symmetries on the synchronized product in order to build the consistent graph. Such symmetries are symmetries of the model and must be an inner symmetries with respect to the considered Büchi automaton.

Let  $A_{\neg f}$  be a Büchi automaton and let M be the structure of the state transition graph. We define the symmetry on the product  $M \times A_{\neg f}$  as follows:

```
Definition 6.2.1: Symmetry \mathcal{R}' defined on M \times A_{\neg f} \not \bowtie_{\mathcal{S}} ' \in \mathcal{S}, \not \bowtie_{\mathcal{S}} ' \in \mathcal{B} such that: s \models Atom(b) and s \models Atom(b'), (s,b)\mathcal{R}(s',b') \Leftrightarrow \exists \pi \in Aut(M), \mathcal{R}_{\pi} \in \mathcal{R}_{in}, b\mathcal{R}_{\pi}b' \land \pi(s) = s'.
```

The relation,  $\mathcal{R}'$ , is used to build a reduced graph  $G' = (AP', \Theta', \tau', \Theta'_0, \Phi)'$  consistent

```
Algorithm 3: Consistent Graph Constructor
BEGIN
   /* Symmetry Computation */
   FOR each i,j from I DO Rij();
   Polynomial computation of \mathcal{R}_{in};
   FOR each equivalence class of \mathcal{R}_{in} of A_{-f} states DO Choose a representative b;
   /* Consistent Graph Construction: */
   FOR each (s_0,b_0) from \Theta_0 such that s_0 \models \hat{b_0} DO BEGIN
           Compute the symbolic representative Rep(s_0, \hat{b_0});
            \Theta' = \Theta' \cup Rep(s_0, \hat{b_0}); Push(Rep(s_0, \hat{b_0}));
   END: /* FOR */
   WHILE Stack is not empty DO BEGIN
           rs = Pop();
           FOR each arc (rs \rightarrow (s,b)) \in \rho DO BEGIN
                   Compute Rep(s,b);
                   IF Rep(s,b) is not in \Theta THEN
                  BEGIN \Theta = \Theta \cup Rep(s,b); Push(Rep(s,b)); END; /* IF */
                   \rho = \rho \cup (rs \rightarrow Rep(s,b));
           END; /* FOR */
   END; /* WHILE */
END. /* ALGORITHM */
```

The complexity of our model checking using symmetries is strongly dependent on the complexity of the former algorithm. Therefore we can deduce its polynomial complexity.

# 7. Conclusion and Perspectives

We have described two frameworks for performing efficient LTL model checking. Both of them exploits the existence of symmetries reflected in the system and in the specification formula to be checked. With the first one, we show how to build the most aggregated structure by using the largest available symmetry relation. Such technique could be computed using algorithms which, in the worst case, would have an exponential complexity. The second framework computes a subset of symmetries with polynomial complexity algorithms inducing a less condensed structure.

In comparison, the method proposed in [6,7] can be considered as a restrictive case requiring the definition of a symmetry group.

Using the symmetry approach, two cases appear as the two extreme limits: the best one where the structural symmetries of the system are entirely used, causing a maximal aggregation of states and the worst case in which any set of symmetrical objects is reduced to a singleton, leading the reduced structure to be as large as the ordinary one.

We now aim at extending our methods to deal with specifications having nothing but partial symmetries [12]. In such specifications, runs sometimes depend on the process

identities (i.e. static priorities based on identities), and sometimes not.

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